Application No.: 10/075,591 Docket No.: 500.41141X00

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Original) An image display having a display area made up of a plurality of pixels and a signal line for feeding a display signal voltage to the pixels, the image display comprising:

a first switch means for inputting the display signal voltage from the signal line to one end of a first capacitance;

an input voltage inversion/output means connected at its input terminal to the other end of the first capacitance;

an illuminating means controlled by an output of the input voltage inversion/output means;

a second switch means provided between the input terminal and an output terminal of the input voltage inversion/output means, wherein the first switch means, the input voltage inversion/output means, the illuminating means and the second switch means are provided in at least one of the plurality of pixels;

a pixel drive voltage generation means for generating a pixel drive voltage, the pixel drive voltage being swept within a predetermined voltage range including the display signal voltage; and

a pixel drive voltage input means for inputting the pixel drive voltage to the one end of the first capacitance in the pixel.

2. (Original) An image display according to claim 1, wherein the illuminating means is a light emitting diode.

3. (Original) An image display according to claim 2, wherein the light emitting diode is an OLED (organic light emitting diode).

4. (Original) An image display according to claim 1, wherein the switch means and the input voltage inversion/output means are formed from polysilicon TFTs (thin-film transistors) on a transparent substrate.

5. (Original) An image display according to claim 1, wherein the input voltage inversion/output means is formed of a CMOS (complementary metal oxide semiconductor) inverter circuit.

6. (Original) An image display according to claim 2, wherein the input voltage inversion/output means is formed of a polysilicon TFT and a light emitting diode as a load.

7. (Original) An image display according to claim 6, wherein a second capacitance is provided between a gate and a source of the polysilicon TFT.

8. (Original) An image display according to claim 1, wherein the pixel drive voltage generated by the pixel drive voltage generation means and swept in the predetermined voltage range is a triangular wave.

9. (Original) An image display according to claim 1, wherein the pixel drive voltage generated by the pixel drive voltage generation means and swept in the predetermined voltage range is a stepped waveform.

10. (Original) An image display according to claim 9, wherein the display signal voltage assumes a virtually median value between two adjoining levels of discretely distributed levels of the stepped waveform of the pixel drive voltage.

11. (Original) An image display according to claim 1, wherein the signal line and the first switch means also serve as the pixel drive voltage input means.

12. (Original) An image display according to claim 1, wherein the pixel drive voltage input means comprises a pixel drive voltage line provided parallel to the signal line and a third switch means provided between the pixel drive voltage line and the one end of the first capacitance.

13. (Currently Amended) An image display according to claim 4, wherein the display signal voltage is generated by a ~~D-A~~digital to analog converter formed of a polysilicon TFT.

14. (Original) An image display according to claim 4, wherein the display signal voltage is generated by a single crystal silicon LSI (large scale integrated circuit).

15. (Original) An image display according to claim 4, wherein the first capacitance is formed of a gate-insulated film capacitance of a polysilicon TFT.

16. (Original) An image display according to claim 1, wherein the pixel drive voltage is swept in synchronism with a timing of writing the display signal voltage for one line of pixels.

17. (Original) An image display according to claim 1, wherein the pixel drive voltage is swept in synchronism with a timing of writing the display signal voltage for a plurality of lines of pixels.

18. (Original) An image display according to claim 1, wherein the pixel drive voltage is swept in synchronism with a timing of writing the display signal voltage for all pixels.

19. (Original) An image display according to claim 1, wherein a sweep repetition frequency of the pixel drive voltage is variable.

20. (Original) An image display according to claim 1, wherein a period in which the pixel drive voltage is applied is alternated with a period in which the display signal voltage for one line of pixels is written.

21. (Original) An image display having a display area made up of a plurality of pixels, a display signal processing circuit for storing a display signal taken

in from outside and processing data of the display signal, and a signal line for feeding a display signal voltage to the pixels, the image display comprising:

a first switch means for inputting the display signal voltage from the signal line to one end of a first capacitance;

an input voltage inversion/output means connected at its input terminal to the other end of the first capacitance;

an illuminating means controlled by an output of the input voltage inversion/output means;

a second switch means provided between the input terminal and an output terminal of the input voltage inversion/output means, wherein the first switch means, the input voltage inversion/output means, the illuminating means and the second switch means are provided in at least one of the plurality of pixels;

a pixel drive voltage generation means for generating a pixel drive voltage, the pixel drive voltage being swept within a predetermined voltage range including the display signal voltage; and

a pixel drive voltage input means for inputting the pixel drive voltage to the one end of the first capacitance in the pixel.

22. (Currently Amended) An image display having a display area made up of a plurality of pixels, pixel drive voltage generation means for generating a pixel drive voltage, and a signal line for feeding a display signal voltage to the pixels, the image display comprising, in at least one of the plurality of pixels:

a memory means for storing the display signal voltage entered from the signal line to the pixel;

voltage supplying means for supplying the pixel drive voltage to the memory means; and

a pixel turn-on period decision means for determining an ON period and an OFF period for an image output in the pixel according to the display signal voltage; and

~~a pixel drive means for repeating an ON operation of the image output a plurality of times in one frame~~.

23. (New) An image display having a display area made up of a plurality of pixels and a signal line to feed a display signal voltage to the pixels, the image display comprising, in at least one of the plurality of pixels:

a memory to store the display signal voltage entered from the signal line to the pixel;

a pixel drive voltage generation circuit to generate a pixel drive voltage to be supplied to the memory;

a voltage supplying circuit to supply the voltage generated by the pixel driving voltage generating circuit to the memory; and

a pixel turn-on period decision circuit to determine an ON period and an OFF period for an image output in the pixel according to both the display signal voltage and the voltage generated by the pixel driving voltage generating circuit and supplied by the voltage supplying circuit to the memory.

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24. (New) An image display according to claim 22, wherein said pixel drive voltage generation means includes means for generating a triangular wave pixel drive voltage.

25. (New) An image display according to claim 23, wherein said pixel drive voltage generation circuit generates a triangular wave pixel drive voltage.

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AMENDMENTS TO THE DRAWINGS

The attached sheets of drawing include changes to Figs. 2, 4 and 16-18. These sheets, which include Figs. 1-4 and 16-18 replace the original sheets including Figs. 1-4 and 16-18. In Figs. 2 and 4, a typographical error has been corrected. In Figs. 16-18, the figures have been identified as prior art. It is noted that these amendments are made in response to the drawing objection set forth in paragraphs 2 and 3 of the Office Action.